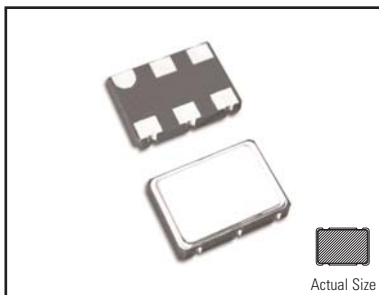


SERIES FMVCLVDSC3S

3.3 Vdc LVDS VCXO

CERAMIC SMD 5x7



- Miniature, Low Profile
- 3.3 Vdc Supply Voltage
- LVDS
- Low Current Consumption

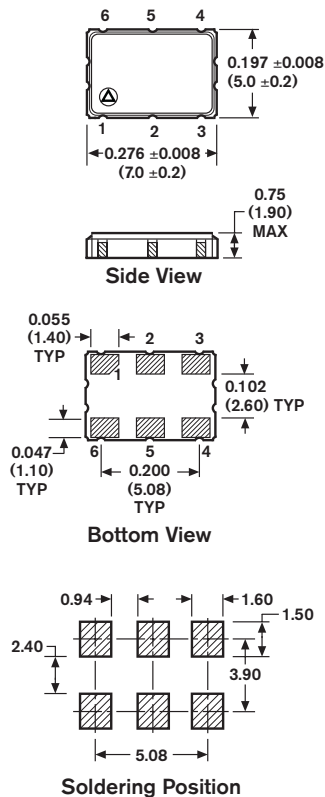
SPECIFICATIONS

05|issue1|012005

Parameter	Specification
Frequency Range	10.000 to 650.000 MHz
Overall Frequency Tolerance	±20 ppm to ±100 ppm (Inclusive of Operating Temp., Supply Voltage, & Load)
Operating Temperature Range	0 to +70°C Std., -40 to +85°C Avail.
Storage Temperature	-55 to +125°C
Supply Voltage (Vdd)	+3.3 Vdc ± 5%
Supply Current (Icc)	80 mA Typical
Phase Jitter (Tpj)	0.044 x 10 ⁻³ Typical
Phase Jitter (Tp-p)	3 pS RMS max. Condition: 12 kHz to 20 MHz
Accumulated Jitter (Tacc)	3 pS RMS max. Condition: 12 kHz to 20 MHz
Symmetry (Duty Cycle)	45/55%
Rise and Fall Time	500 ps Typ. / 800 ps max.
Start Time	10 ms max. < 5 ms typical
Output	LVDS
Output Load	100 ohms max. (Vcc - 2.0V)
Differential Output Voltage	0.247V to 0.454V
(Vod) (Out 1 - Out 2)	(0.33V Typical)
Offset Voltage	1.25 Vos
Pin 1 Options (See Spec. Option T below)	No Connect (Std.) Tri-State (option T) VIH: 1.6 V max. or Open Enables Output VIL: 0.9 V min. Disables Output Output Disable/Enable Time: 100 nS max.
Aging @ 25°C	±5ppm max first year

All specifications subject to change without notice.

CERAMIC SMD



PIN FUNCTION TABLE

Pin	Function
1	Control V
2	Tri-State Control
3	Ground (Case)
4	Output
5	Diff. Output
6	Supply Voltage (Vdd)

NOTE: Waveforms & Test Circuits on pages 48, 49

Standard Specifications for product indicated in color

Dimensions: $\frac{\text{Inches}}{\text{(mm)}}$

PART NUMBERING SYSTEM

MARKING: See Page 57, Format M

