

FMOCJS SERIES

5.0 Vdc Clock Oscillators

J-LEAD PLASTIC SMD

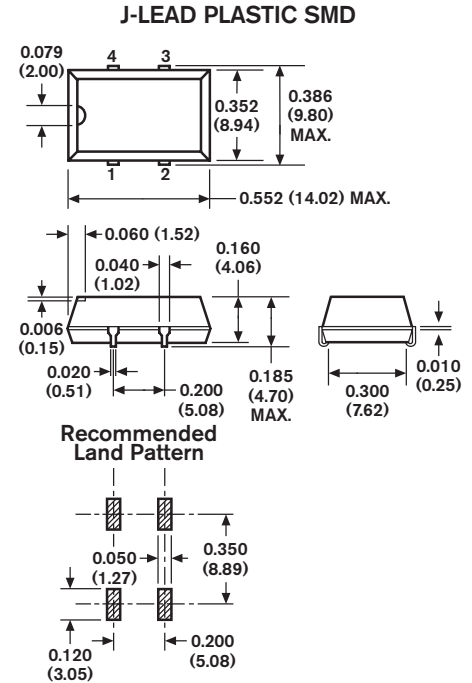


- HCMOS/TTL Compatible
- J-Lead Molded Plastic
- Surface Mount
- Tape and Reel Available

05|issue1|110904

Parameter	Specification
Frequency Range	1.544 - 180 MHz
Overall Frequency Tolerance	±25 ppm to ±100 ppm (Inclusive of Operating Temp., Supply Voltage, & Load)
Operating Temperature Range	0 to +70°C Std.
Storage Temperature	-55 to +125°C
Supply Voltage (Vdd)	+5.0 Vdc (±0.5 Vdc)
Supply Current (Icc)	20 mA max. @ 1.0 to 23.9 MHz 40 mA max. @ 24.0 to 49.9 MHz 50 mA max. @ 50.0 to 66.7 MHz 70 mA max. @ 67.0 to 180.0 MHz
Symmetry (Duty Cycle)	40/60% Std., 45/55% Avail. (See Spec. Option S)
Output "0" Level (VOL)	0.4 Vdc max. (TTL) 0.5 Vdc max. (HCMOS)
Output "1" Level (VOH)	2.4 Vdc min. (TTL) 4.5 Vdc min. (HCMOS)
Rise and Fall Time	10 ns max. < 5 ns typical
Start Time	10 ms max. < 5 ms typical
Output Load	30 pF HCMOS Load Max.
Pin 1 Options (See Spec. Option T below)	No Connect (Std.) Tri-State (option T) VIH: 2.2 V or Open Enables Output VIL: 0.8 V Disables Output Output Disable/Enable Time: 100 nS max.
Aging @ 25°C	±5ppm max first year

All specifications subject to change without notice.



PIN FUNCTION TABLE

Pin	Function
1	NC or Tri-State
2	Case Ground
3	Output
4	Supply Voltage (Vdd)

Dimensions: $\frac{\text{Inches}}{\text{(mm)}}$

NOTE: Waveforms & Test Circuits on pages 48, 49

Standard Specifications for product indicated in **color**

PART NUMBERING SYSTEM

MARKING: See Page 57, Format P

