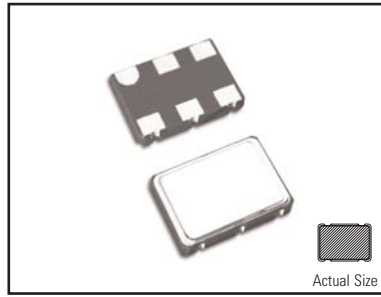


# SERIES FMOCLVDSC2S

2.5 Vdc LVDS Clock Oscillators

**CERAMIC SMD 5x7**



- Miniature, Low Profile
- 2.5 Vdc Supply Voltage
- LVDS
- Low Current Consumption

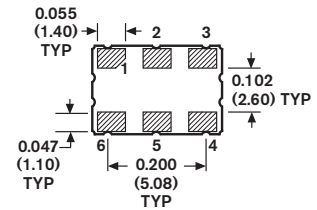
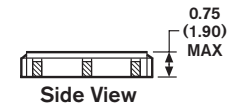
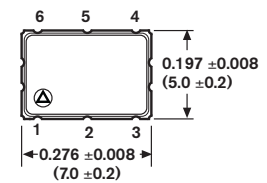
## SPECIFICATIONS

05 | ISSUE 1 | 012005

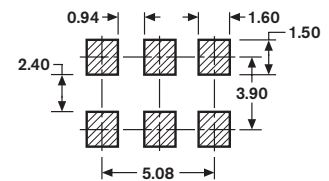
Parameter	Specification
Frequency Range	10.000 to 250.000 MHz
Overall Frequency Tolerance	±20 ppm to ±100 ppm (Inclusive of Operating Temp., Supply Voltage, & Load)
Operating Temperature Range	0 to +70°C Std., -40 to +85°C Avail.
Storage Temperature	-55 to +125°C
Supply Voltage (Vdd)	+2.5 Vdc ± 5%
Supply Current (Icc)	20 - 40 mA Typical (70 mA max.)
Phase Jitter (Tpj)	0.044 x 10 <sup>-3</sup> Typical
Phase Jitter (Tp-p)	3 pS RMS max.   Condition: 12 kHz to 20 MHz
Accumulated Jitter (Tacc)	3 pS RMS max.   Condition: 12 kHz to 20 MHz
Symmetry (Duty Cycle)	45/55%
Rise and Fall Time	300 ps (20 to 80% of Vod) (700 ps max.)
Start Time	10 ms max. < 5 ms typical
Output	LVDS
Output Load	50 ohms max. (Vcc - 2.0V), 100 ohms max. available
Differential Output Voltage	0.247V to 0.454V
(Vod) (Out 1 - Out 2)	(0.33V Typical)
Offset Voltage	1.25 Vos
Pin 1 Options (See Spec. Option T below)	No Connect (Std.) Tri-State (option T) VIH: 2.2 V or Open Enables Output VIL: 0.8 V Disables Output Output Disable/Enable Time: 100 nS max.
Aging @ 25°C	±5ppm max first year

All specifications subject to change without notice.

### CERAMIC SMD



### Bottom View



### Soldering Position

### PIN FUNCTION TABLE

Pin	Function
1	Tri-state Enable/Disable
2	NC or Tri-State
3	Ground (Case)
4	Output
5	Diff. Output
6	Supply Voltage (Vdd)

## NOTE: Waveforms & Test Circuits on pages 48, 49

Standard Specifications for product indicated in **color**

Dimensions:  $\frac{\text{Inches}}{\text{(mm)}}$

## PART NUMBERING SYSTEM

## MARKING: See Page 57, Format M

